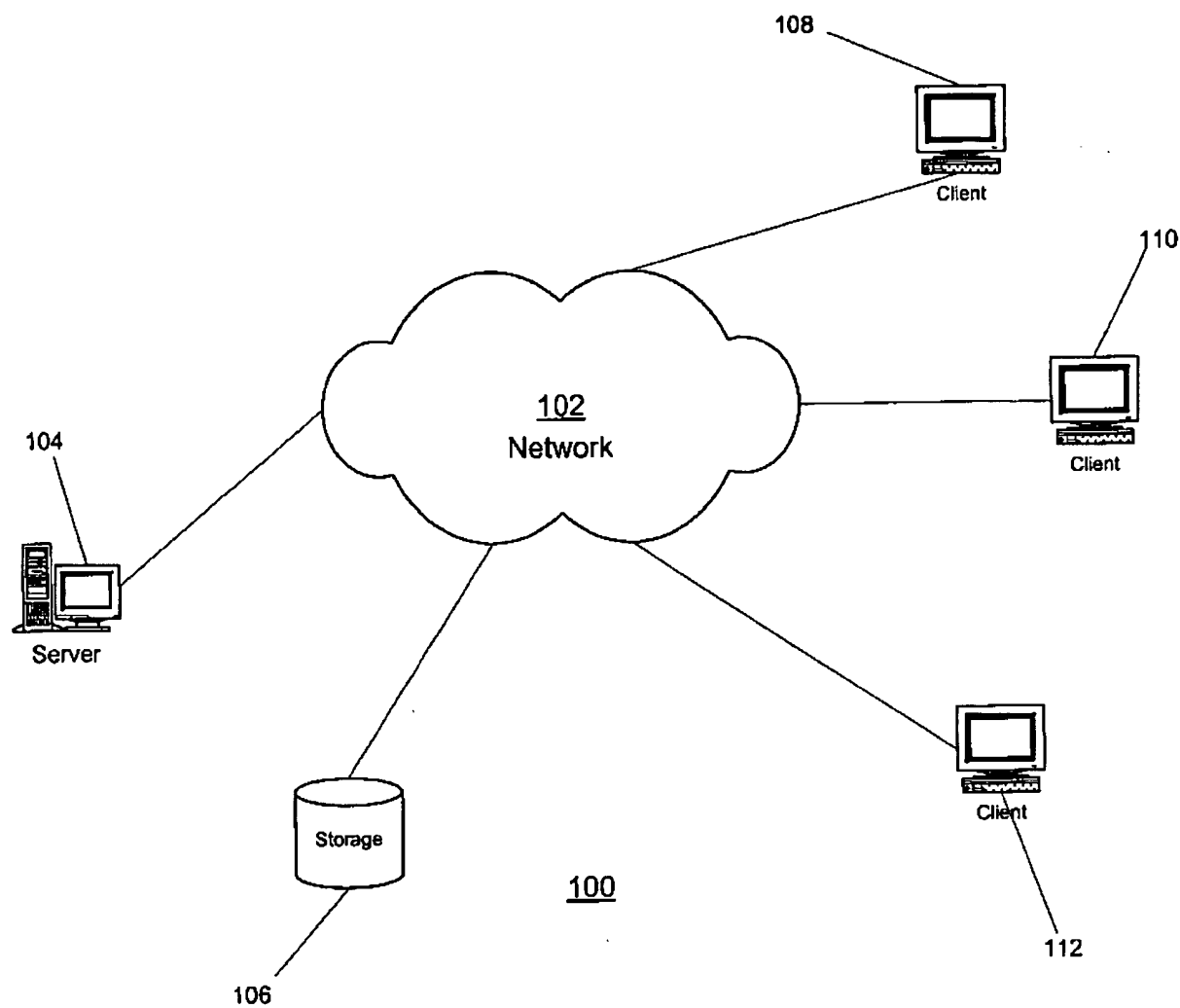


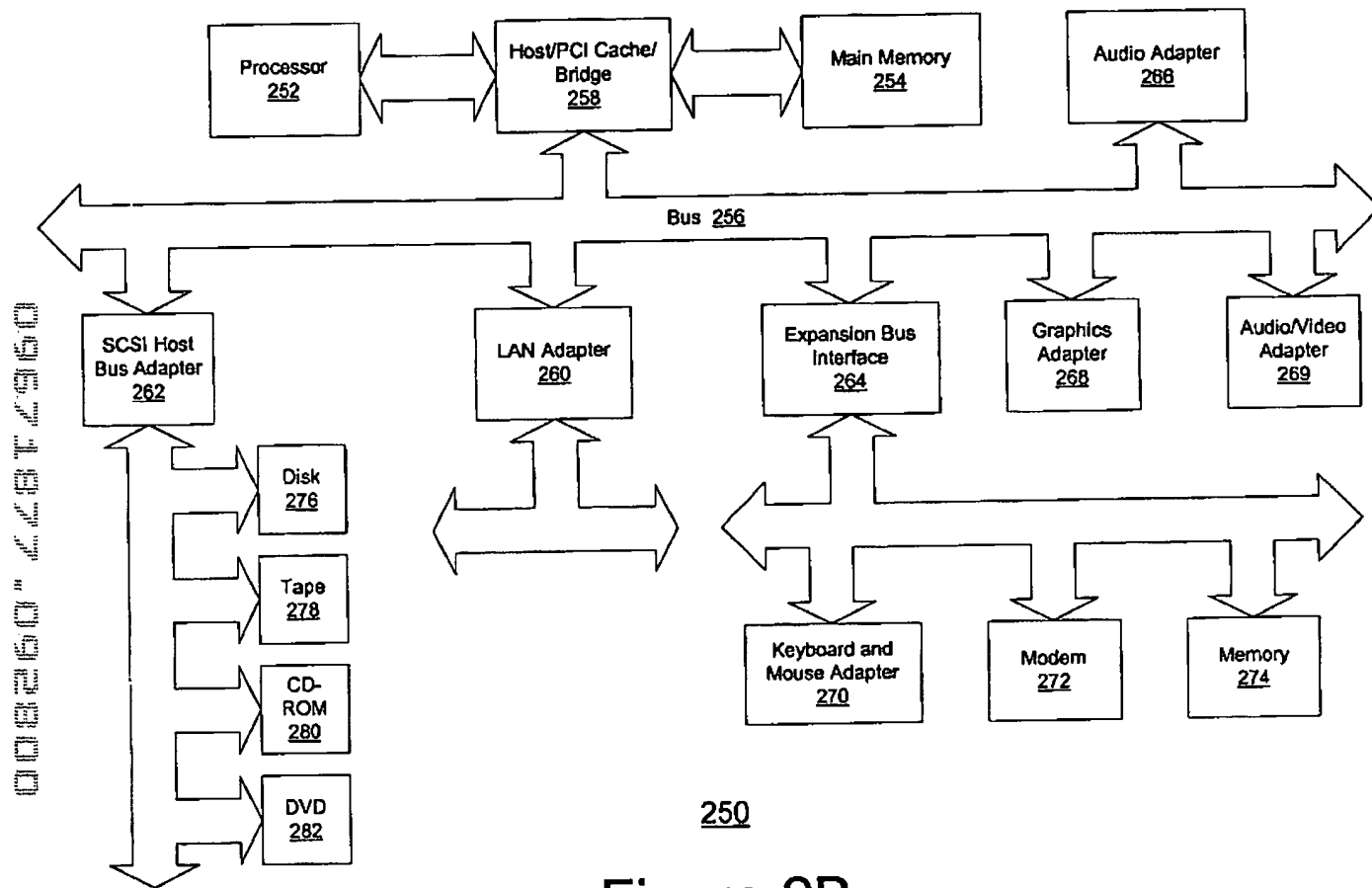
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**Figure 1**

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The diagram illustrates a computer system architecture. At the top, two processors, Processor 202 and Processor 204, are connected to a horizontal System Bus (206). Below the system bus is a block containing a Memory Controller/Cache (208) and an I/O Bridge (210). The Memory Controller/Cache (208) is connected to Local Memory 209. The I/O Bridge (210) is connected to a vertical I/O Bus (212). This I/O Bus (212) branches out to connect to several components: a Graphics Adapter 230, a Hard Disk 232, a PCI Bus Bridge 214, a PCI Bus Bridge 222, and a PCI Bus Bridge 224. The PCI Bus Bridge 214 is connected to a PCI Bus (216), which in turn connects to a Modem (218) and a Network Adapter (220). The PCI Bus Bridge 222 is connected to a PCI Bus (226). The PCI Bus Bridge 224 is connected to a PCI Bus (228). The entire system is labeled with the reference numeral 200.

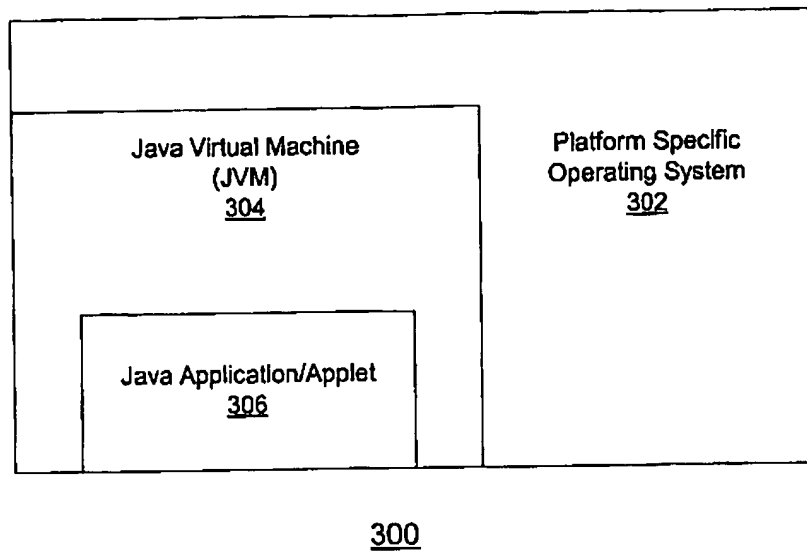
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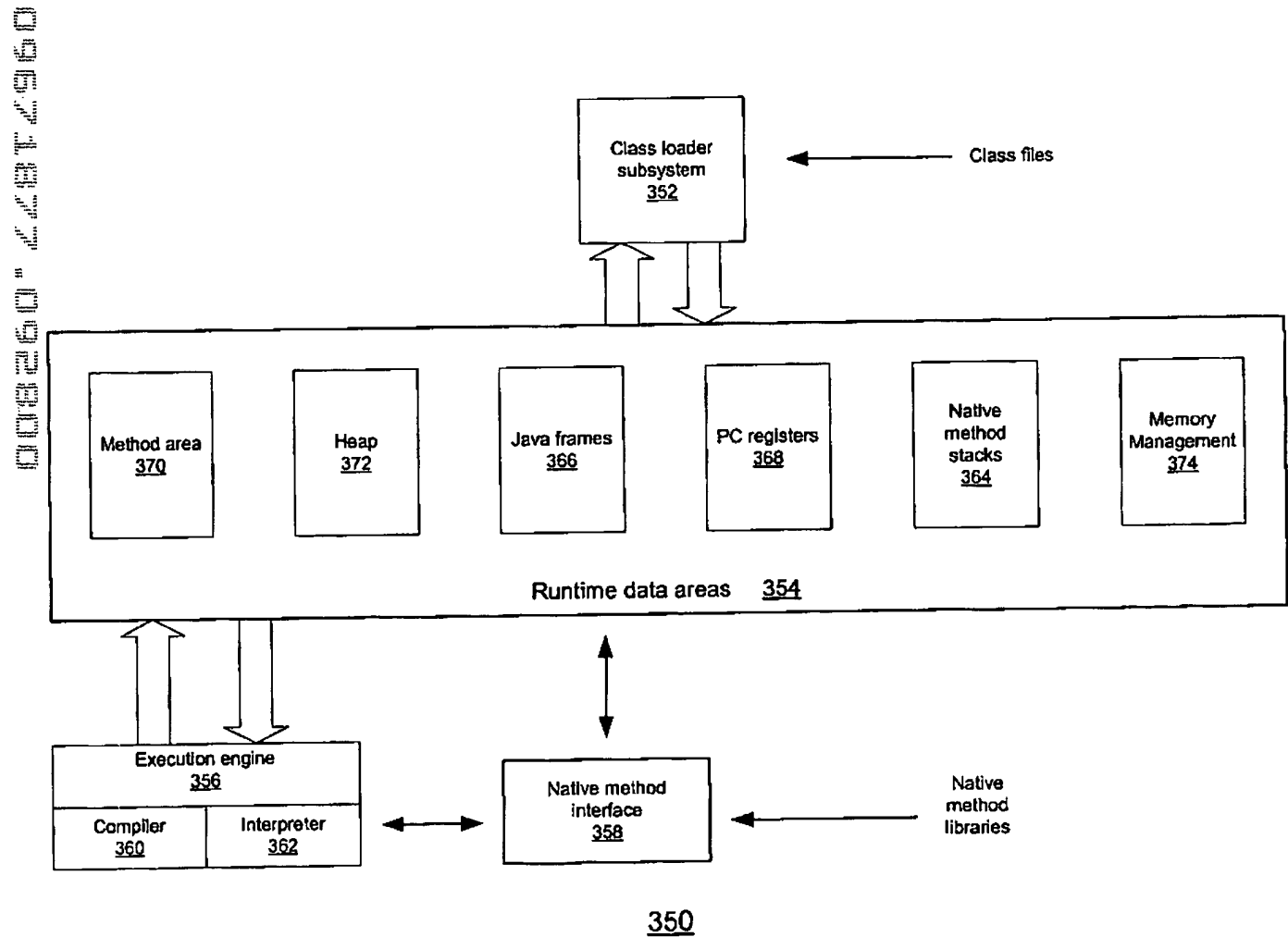
Figure 2B

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**Figure 3A**

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**Figure 3B**

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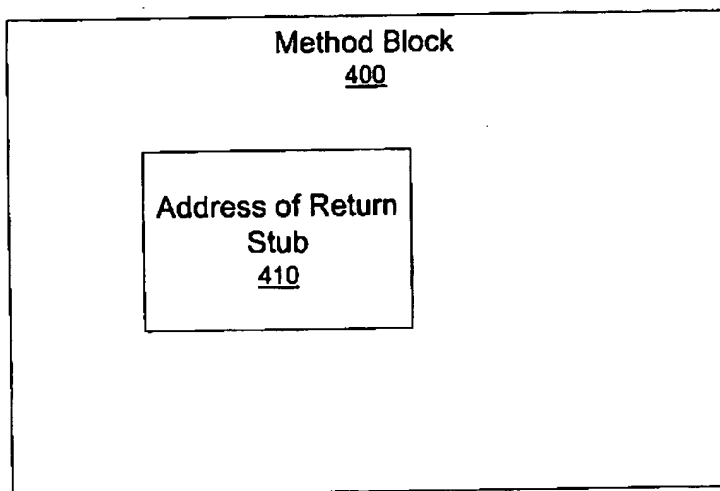
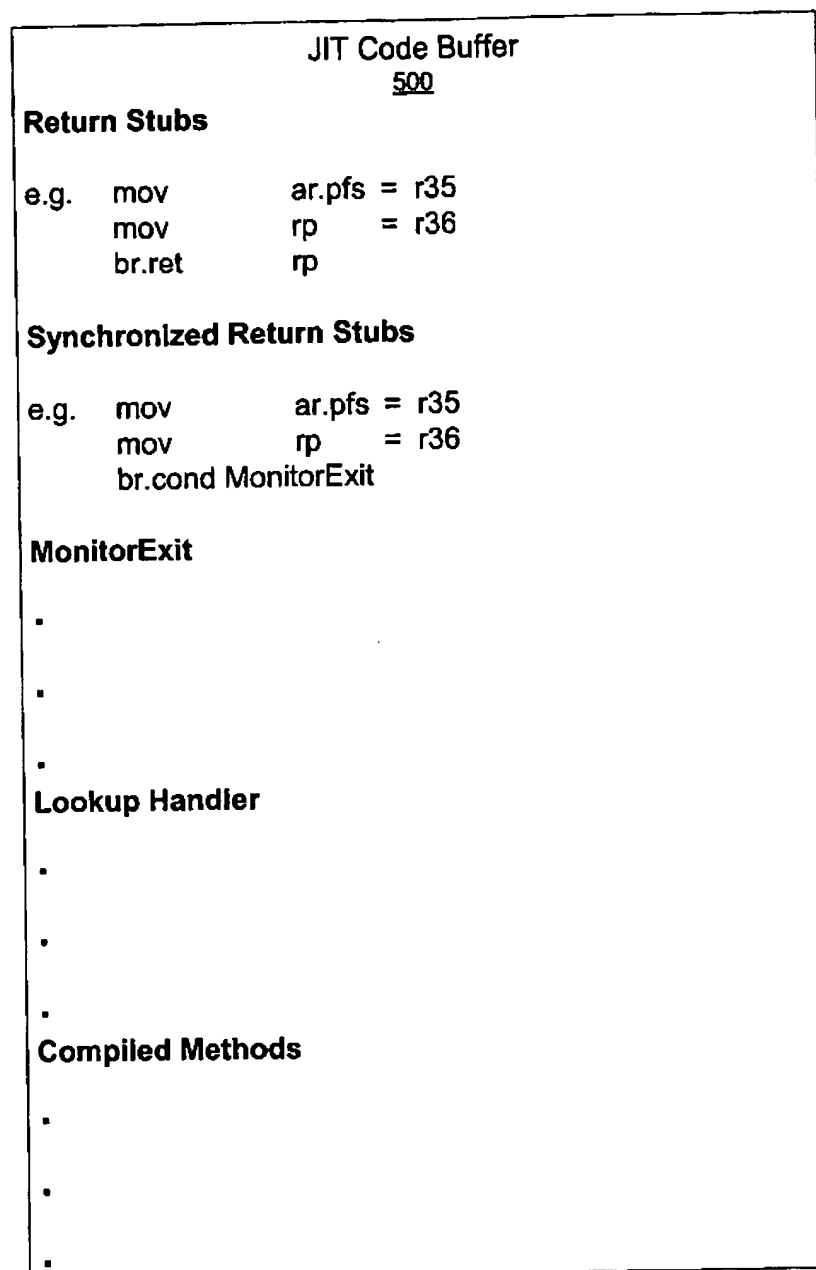


Figure 4

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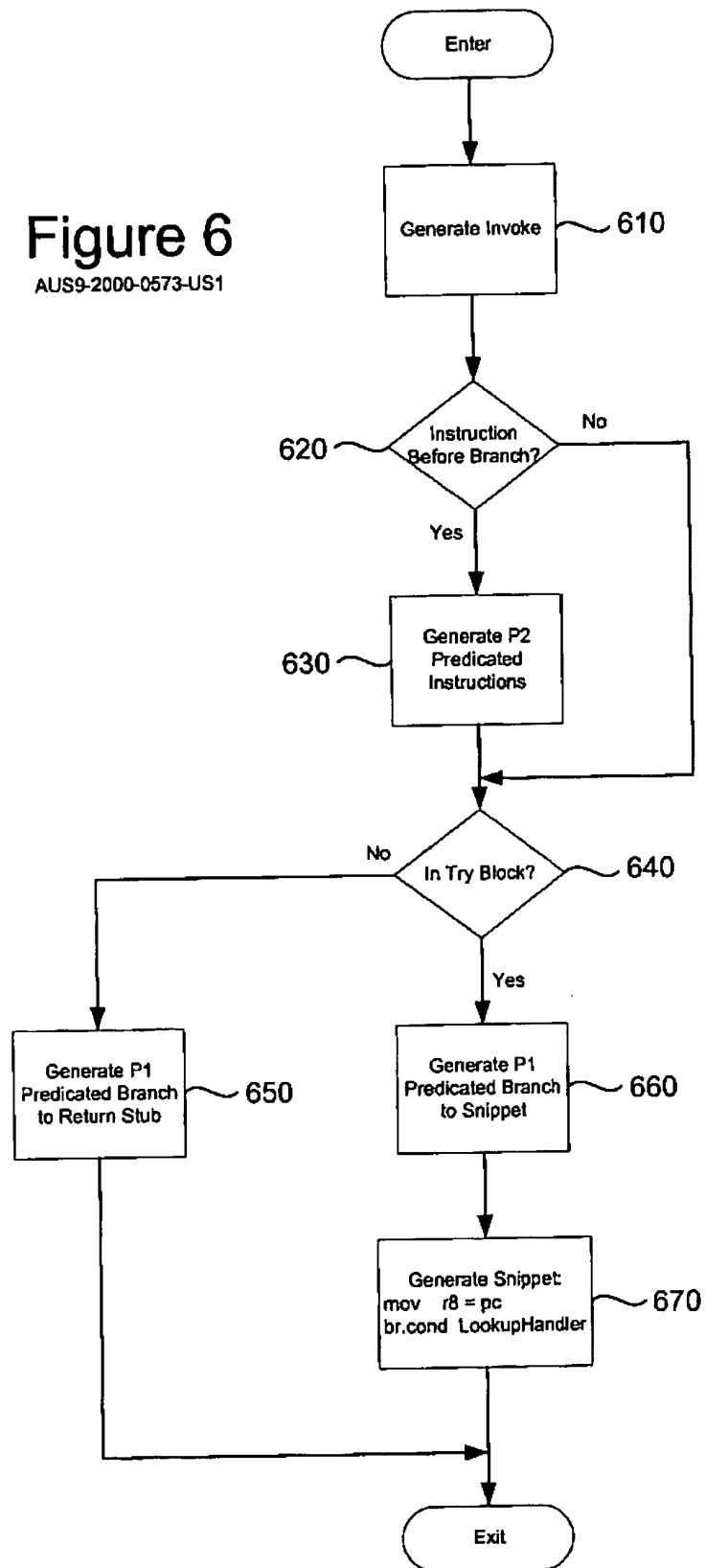
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**Figure 5**

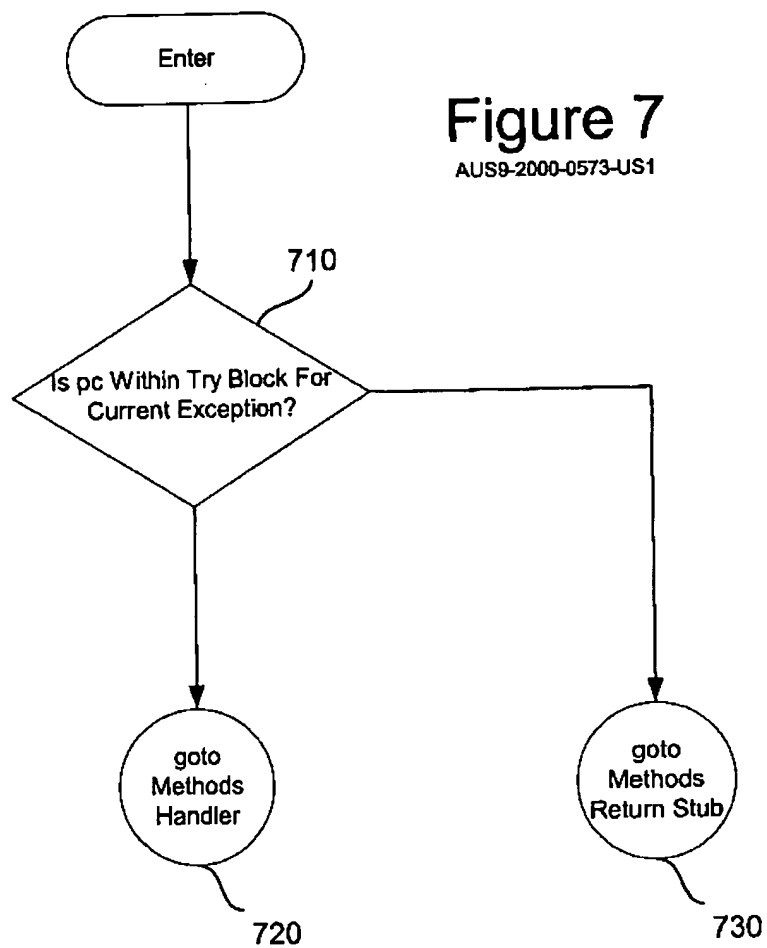
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**Figure 6**  
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**SECRET**



**Figure 7**  
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